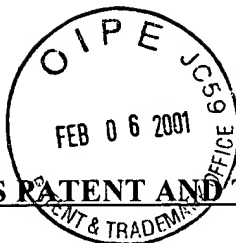


S/N 09/510,375



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brett L. Williams

Examiner: Hong Kim

Serial No.: 09/510,375

Group Art Unit: 2185

Filed: February 22, 2000

Docket: 303.164US3

Title: SYSTEM SUPPORTING MULTIPLE MEMORY MODES INCLUDING A  
BURST EXTENDED DATA OUT MODE (AS AMENDED)

6/AB  
JK  
2-10-01

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents  
Washington, D.C. 20231

RECEIVED  
FEB 13 2001  
Technology Center 2100

Applicant has reviewed the Office Action mailed on December 29, 2000. Detailed comments to the Office Action are found in the Remarks section below. Please amend the above-identified patent application as follows:

IN THE TITLE

Please replace the title with the following new title:

--System Supporting Multiple Memory Modes Including a Burst Extended Data Out  
Mode--.

02/09/2001 TGEDAMU1 00000079 190743 09510375

01 FC:102

160.00 OP

IN THE CLAIMS

Please amend the claims as follows:

32. (Once Amended) A system, comprising:
- a bus for transferring information;
  - a memory, coupled to the bus, comprised of a memory device [having a first operation]  
operable in a mode selected from the group consisting of burst extended data out  
mode and a second operation mode, wherein the memory has a first set of access  
control signals for operation in the [first operation] burst extended data out mode  
and a second set of access control signals for operation in the second operation  
mode;
  - a programmable memory controller, coupled to the bus and to the memory, capable of  
providing the first set of access control signals and the second set of access  
control signals to the memory; and
  - a processor, coupled to the bus and the memory controller, responsive to at least

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